## C.U. SHAH UNIVERSITY Summer Examination-2022

## Subject Name : Advanced Micro-Processors

	Subject C	Code : 4TE05AMP1Branch: B.Tech (CE)	
	Semester: Instruction (1) U		
	(3) D	nstructions written on main answer book are strictly to be obeyed. For near diagrams and figures (if necessary) at right places. Assume suitable data if needed.	
Q-1		Attempt the following questions:	
	<b>a</b> )	Define : Embedded system.	(01)
	<b>b</b> )	Define : Microprocessor.	(01)
	<b>c</b> )	Define : Microcontroller.	(01)
	d) e)	True/False: 8086 is 16-byte microprocessor. What is addressing mode?	(01) (01)
	f)	Which group of instructions does affect the flags?	(01)
		<ul><li>A). Arithmetic operations</li><li>B). Logic operations</li><li>C). Data transfer operations</li><li>D). Both A and B.</li></ul>	(01)
	<b>g</b> )	MVI A, 02 instructions is how many bits of instruction?	(01)
	1.)	A). 32 B).16 C). 8 D). 24	
	h)	Intel 80386 microprocessor is a processor.A). 4 bitB). 32 bitC).8 bitD). 16 bit	(01)
	i)	Which instruction is used to read content from memory and store it in accumulator?	(01)
	•	A) STA B) LDA C) RDA D) MDA	
	<b>j</b> )	CMA is how many byte of instruction? A). 1 B). 2 C). 3 D). 4	(01)
	k)	The first microprocessor built by the Intel Corporation was calledA) 8080B) 8008C) 4004D) 8800	(01)
	l)	The PF is known asA) Priority flagB) positive flagC) Parity flagD) push flag	(01)
	m)	In which mode 80386 do support memory management and protection mechanism? A). Direct mode B). indirect mode C). Protected mode D). Real mode The selector contains the segment's	(01)
	n)	A). Segment limit B). Base address C). Access right byte D). All of the above	(01)

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## Attempt any four questions from Q-2 to Q-8

	Attempt all questions	
<b>(a)</b>		(07)
<b>(b)</b>	What is descriptor table? Explain GDT, LDT, IDT.	(07)
	Attempt all questions	
<b>(a)</b>		(07)
<b>(b</b> )	Draw and explain architecture of 8085 MP.	(07)
	Attempt all questions	
<b>(a)</b>	Draw pin diagram of 8085 MP. Explain following pins 1. HOLD 2. RESET	(07)
<b>(-</b> )		
(b)	Draw and explain Programming and hardware model of 8085 MP.	(07)
	Attempt all questions	
(a)		(07)
(b)	Explain Real Address Mode and Protected Virtual Address Mode of 80386 MP.	(07)
	Attempt all questions	
(a)	Explain different type of interrupt maskable, non-maskable, vector, non-vector	(07)
<b>(b)</b>	Explain different types of instruction of 8085 as per functionality.	(07)
	Attempt all questions	
<b>(a)</b>		(07)
(b)	Draw and Explain Memory bank system of 8086 MP.	(07)
	Attempt all questions	
(a)		(07)
		(07)
	<ul> <li>(a)</li> <li>(b)</li> <li>(a)</li> <li>(b)</li> <li>(a)</li> <li>(b)</li> <li>(a)</li> <li>(b)</li> <li>(a)</li> <li>(a)</li> <li>(b)</li> </ul>	<ul> <li>(b) What is descriptor table? Explain GDT, LDT, IDT.</li> <li>Attempt all questions <ul> <li>(a) Draw and explain bus system of 8085 MP.</li> <li>(b) Draw and explain architecture of 8085 MP.</li> </ul> </li> <li>(b) Draw and explain architecture of 8085 MP.</li> </ul> <li>(a) Draw pin diagram of 8085 MP. Explain following pins 1. HOLD 2. RESET <ul> <li>3. ALE 4. IO/M 5. READY.</li> <li>(b) Draw and explain Programming and hardware model of 8085 MP.</li> </ul> </li> <li>Attempt all questions <ul> <li>(a) Write short note memory protection mechanism in x86 families.</li> <li>(b) Explain Real Address Mode and Protected Virtual Address Mode of 80386 MP.</li> </ul> </li> <li>Attempt all questions <ul> <li>(a) Explain different type of interrupt maskable, non-maskable, vector, non-vector with example.</li> <li>(b) Explain different types of instruction of 8085 as per functionality.</li> </ul> </li> <li>Attempt all questions <ul> <li>(a) Draw Register set of 80386 processor and explain flag register.</li> <li>(b) Draw and Explain Memory bank system of 8086 MP.</li> </ul> </li>



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